



THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Jung-hyun LEE et al.

Art Unit: 2823

Serial No. 10/830,120

Examiner: Jarrett J. Stark

Filed: April 23, 2004

Confirmation No. 9015

For: STACK-TYPE CAPACITOR,
SEMICONDUCTOR MEMORY DEVICE
HAVING THE SAME, AND METHODS OF
MANUFACTURING THE CAPACITOR AND
THE SEMICONDUCTOR MEMORY
DEVICE

Attorney Docket No. 249/456

REPLY UNDER 37 C.F.R. § 1.114

Mail Stop: RCE
Commissioner for Patents
United States Patent and Trademark Office
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

INTRODUCTORY COMMENTS

In response to the Office Action Made Final mailed on August 18, 2006, and the October 31, 2006 Advisory Action, and further to the concurrently filed Request for Continued Examination, the following remarks are respectfully submitted in connection with the above-identified application:

A listing of the Claims begins on page 2 of this paper; and

Remarks begin on page 7 of this paper.